Electronic Pater	nt Applicati	on Fe	e Transm	ittal			
Application Number:	10557104	10557104					
Filing Date:	27-Nov-200	27-Nov-2006					
Title of Invention:	Integrated (	Integrated circuit with bit error test capability					
First Named Inventor/Applicant Name:	Martin Heir	Martin Heinen					
Filer:	Marc Boby	Marc Bobys/See Woon Ng					
Attorney Docket Number:	20031035-	20031035-02					
Filed as Large Entity							
U.S. National Stage under 35 USC 371	Filing Fees						
Description	Fee	Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:	<u> </u>						
Pages:							
Claims:			16				
Independent claims in excess of 3		614	1	210	210		
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
	Tota	1020		